L Number	Hits	Search Text	DB	Time stamp
2	1	(core with logic with register) same shell	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 08:14
3	1	(core with logic) same register same shell	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 08:14
4	2	core same logic same register same shell	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 07:58
5	1288	(core with logic) same register	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 08:15
6	9	(shell with module) and ((core with logic) same register)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 08:15
15	2	("6205572").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 14:43
16	0	(delay same buffer) and (("6205572").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 14:43
22	1	(delay same buffer) and (("6205572").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 14:47
23	1	(timing same buffer$3) and (("6205572").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/02 14:47
-	1	("6714939").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:37
-	1	("6714902").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:41
-	2	("6167359").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 13:47
-	1064	(synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/29 15:44
-	270	((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 17:56
-	93	(partition$3 or divid$3) and (core or shell) and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:44

-	18	(partition$3 or divid$3) and core and shell and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:03
-	143	((floorplann$3 or (floor adj1 plann$3) or placement or placing) same optimiz$5) and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:43
-	58	(partition$3 or divid$3) and (core or shell) and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same optimiz$5) and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell)))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 14:45
-	192	(floorplann$3 or (floor adj1 plann$3) or placement or placing or (physical adj1 design)) with synthesi$4 with (before of after)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 18:04
-	13	((partition$3 or divid$3) and (core or shell) and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))) and ((floorplann$3 or (floor adj1 plann$3) or placement or placing or (physical adj1 design)) with synthesi$4 with (before of after))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 18:03
-	9	(resynthesi$4 or re-synthesi$4 or recombin$3) and (((partition$3 or divid$3) and (core or shell) and (((floorplann$3 or (floor adj1 plann$3) or placement or placing) same (block or unit or sub-circuit or (sub adj1 circuit) or cell)) and ((synthesiz$3 same circuit) and (optimiz$5 same (block or unit or sub-circuit or (sub adj1 circuit) or cell))))) and ((floorplann$3 or (floor adj1 plann$3) or placement or placing or (physical adj1 design)) with synthesi$4 with (before of after)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/02 18:05
-	25424	(synthesi$4 same (partition$3 or divid$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:34
-	78	((synthesi$4 same (partition$3 or divid$3))) and (optimiz$5 near2 plac$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 08:35
-	20	(layer same length) and (((synthesi$4 same (partition$3 or divid$3))) and (optimiz$5 near2 plac$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 08:35
-	20	(buffer or repeater) and ((layer same length) and (((synthesi$4 same (partition$3 or divid$3))) and (optimiz$5 near2 plac$5)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 08:36

-	11	((pin or point or port) same wir$3) and ((buffer or repeater) and ((layer same length) and (((synthesi$4 same (partition$3 or divid$3))) and (optimiz$5 near2 plac$5))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:32
-	1216	(deep or sub-micron or (sub adj1 micron)) near2 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:33
-	1132	(deep or sub-micron or (sub adj1 micron)) adj4 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:33
-	40	synthesi$4 and (partition$3 or divid$3) and ((deep or sub-micron or (sub adj1 micron)) adj4 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:11
-	16	((wir$3 or net or line or conductor or lead) with layer) and (synthesi$4 and (partition$3 or divid$3) and ((deep or sub-micron or (sub adj1 micron)) adj4 circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/05 10:39
-	8	(("6496972") or ("6438731") or ("6360356") or ("5870308")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/29 15:40
-	2	"6622291"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/29 15:40
-	4	register and ((("6496972") or ("6438731") or ("6360356") or ("5870308")).PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/29 15:41
-	4	(partition$3 or divi$4 or distribut$3) and (register and ((("6496972") or ("6438731") or ("6360356") or ("5870308")).PN.))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/29 15:43
-	3	synthesiz$3 and ((partition$3 or divi$4 or distribut$3) and (register and ((("6496972") or ("6438731") or ("6360356") or ("5870308")).PN.)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/29 15:44
-	41663	core same shell	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:13
-	5769	synthesi$4 and (core same shell)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:12
-	1174	optimiz$5 and (synthesi$4 and (core same shell))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:13
-	164	(core same shell same (partition$3 or divi$4 or distribut$3)) and (optimiz$5 and (synthesi$4 and (core same shell)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:47
-	40	(core same shell same synthesi$4) and ((core same shell same (partition$3 or divi$4 or distribut$3)) and (optimiz$5 and (synthesi$4 and (core same shell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:21
-	7898	716/.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:22
-	0	((core same shell same (partition$3 or divi$4 or distribut$3)) and (optimiz$5 and (synthesi$4 and (core same shell)))) and 716/.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:22

–	5	register and ((core same shell same (partition$3 or divi$4 or distribut$3)) and (optimiz$5 and (synthesi$4 and (core same shell))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:23
–	14	(optimiz$5 and (synthesi$4 and (core same shell))) and 716/.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:23
–	9	(partition$3 or divi$4 or distribut$3) and ((optimiz$5 and (synthesi$4 and (core same shell))) and 716/.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/11/01 14:48